

Fig 1

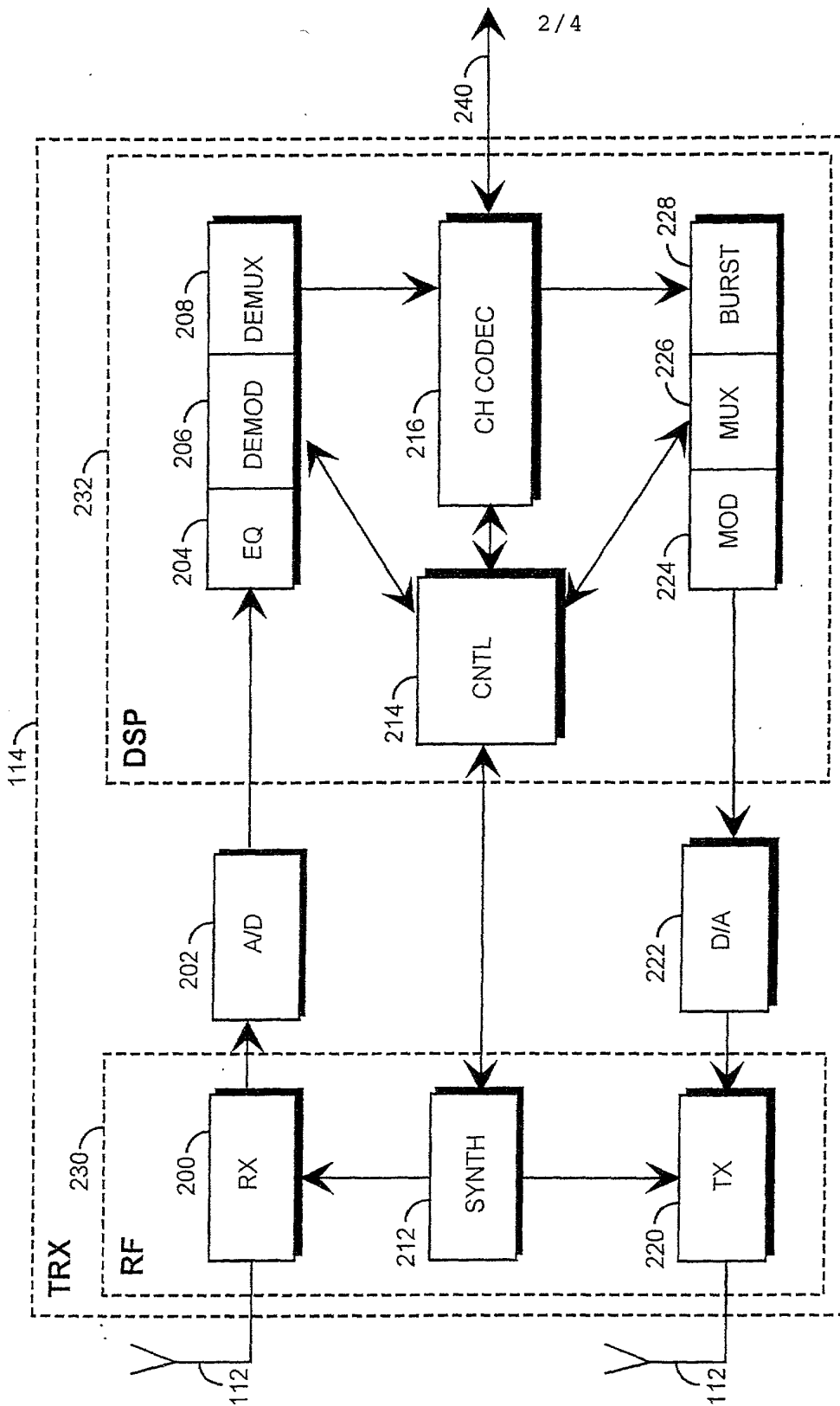


Fig 2

FIG. 3 is a block diagram of a system for time stamping data. The system includes a reference clock (302) and a time stamp generator (306) that generate a reference time (304) and a time stamp (308). The time stamp is sent (310) to a time stamp sender (312). The time stamp sender (312) transmits the time stamp (308) to a time stamp receiver (314). The time stamp receiver (314) provides a time stamp (308) to a device calculator (316). The device calculator (316) also receives a deviation (318) from a device variable (320). The device calculator (316) outputs a deviation (318) to a speed correction factor generator (322). The speed correction factor generator (322) outputs a speed correction factor (324) to a correction block (326). The correction block (326) outputs a speed correction factor (324) to a local clock (330). The local clock (330) outputs a clock (334) to a synthesizer (212).

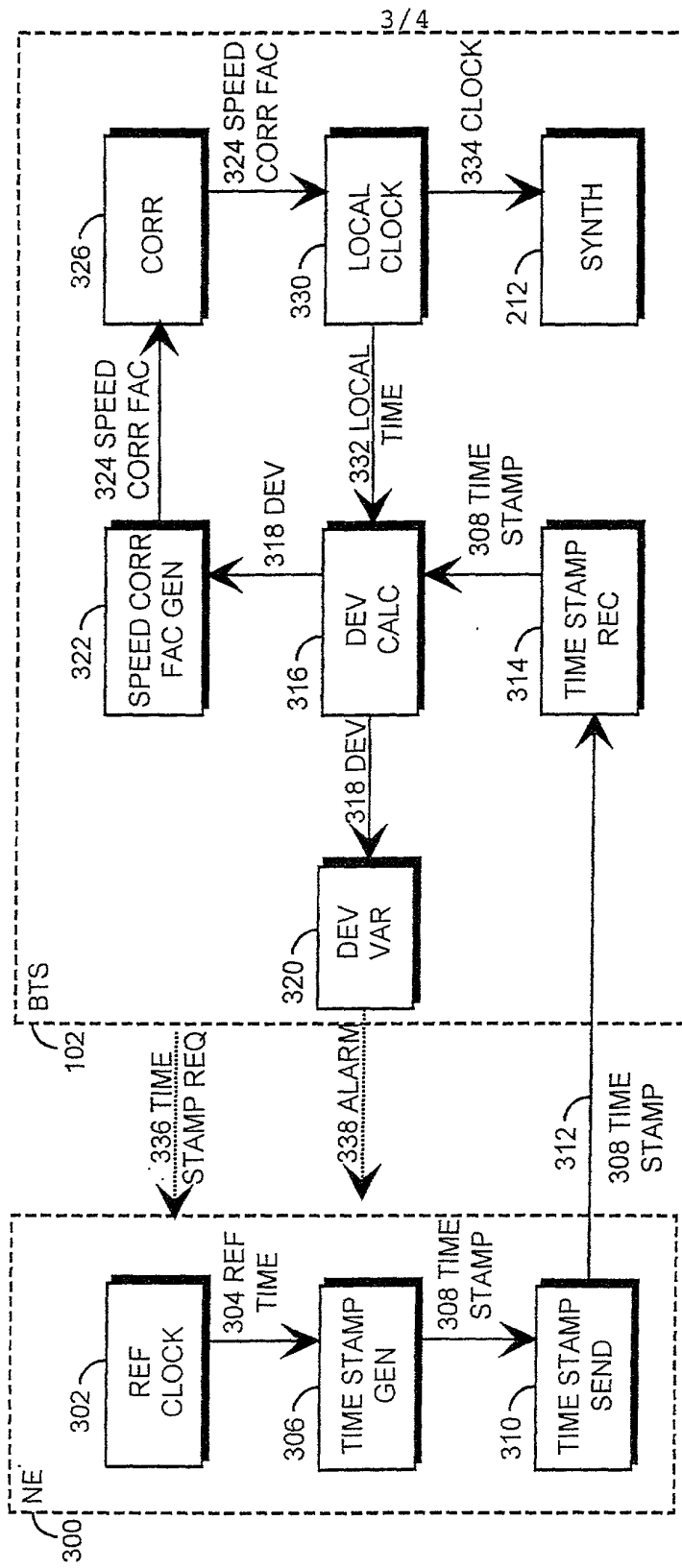


Fig 3

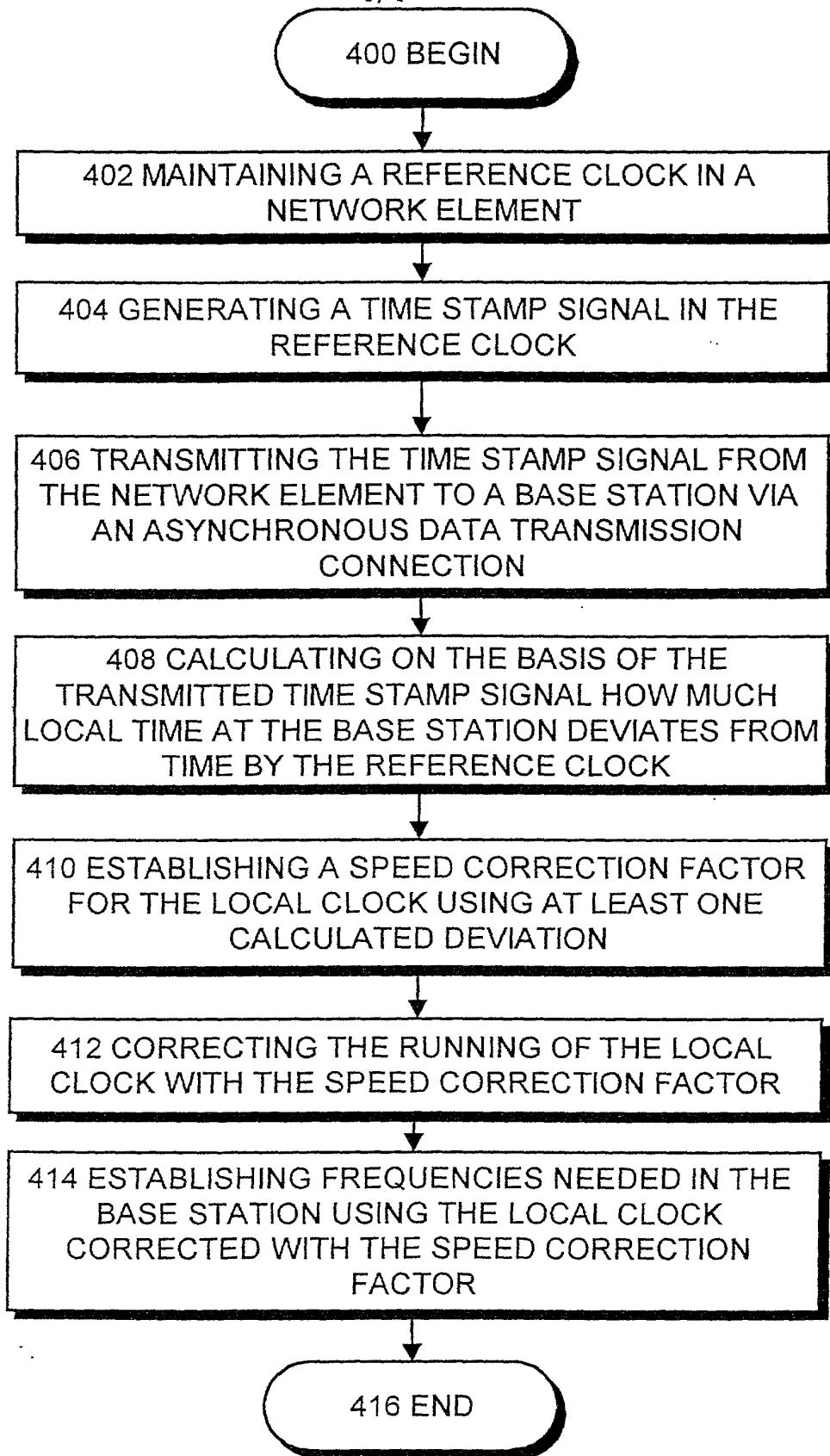


Fig 4